

	T Y P e	Hits	Search Text	DBs	Time Stamp	Com men ts	Er ro r D e f i n i t i o n	Er ro rs
1	B R S	226095	transistor or transistors	USP AT	2003/01/29 09:09			0
2	B R S	39575	differential near2 (amplifier or amplifiers or amplifying)	USP AT	2003/01/29 09:09			0
3	B R S	17536	level near2 (shift or shifting or shifter or shifts or shifted or shifters)	USP AT	2003/01/29 09:10			0
4	B R S	1103132	parallel or paralleled or parallels	USP AT	2003/01/29 09:11			0
5	B R S	313613	offset or offsets or offsetting	USP AT	2003/01/29 09:11			0
6	B R S	13323	common near2 (mode or modes)	USP AT	2003/01/29 10:33			0
7	B R S	56	((transistor or transistors) and (differential near2 (amplifier or amplifiers or amplifying)) and (level near2 (shift or shifting or shifter or shifts or shifted or shifters)) and (parallel or paralleled or parallels) and (offset or offsets or offsetting) and (common near2 (mode or modes))) and (330/\$.ccls.)	USP AT	2003/01/29 09:13			0
8	B R S	217	(transistor or transistors) and (differential near2 (amplifier or amplifiers or amplifying)) and (level near2 (shift or shifting or shifter or shifts or shifted or shifters)) and (parallel or paralleled or parallels) and (offset or offsets or offsetting) and (common near2 (mode or modes))	USP AT	2003/01/29 11:01			0
9	B R S	1230	input near2 common near2 (mode or modes)	USP AT	2003/01/29 10:34			0

	T Y P e	Hits	Search Text	DBs	Time Stamp	Com men ts	Er ro r De fi ni ti on	Er ro rs
10	B R S	74	(transistor or transistors) and (differential near2 (amplifier or amplifiers or amplifying)) and (level near2 (shift or shifting or shifter or shifts or shifted or shifters)) and (parallel or paralleled or parallels) and (input near2 common near2 (mode or modes))	USP AT	2003/01/29 11:01			0
11	B R S	0	(transistor or transistors) and (differential near2 (amplifier or amplifiers or amplifying)) and (level near2 (shift or shifting or shifter or shifts or shifted or shifters)) and (parallel or paralleled or parallels) and (offset or offsets or offsetting) and (common near2 (mode or modes))	EPO ; JPO ; DER WEN T; IBM _TD _B	2003/01/29 11:01			0
12	B R S	0	(transistor or transistors) and (differential near2 (amplifier or amplifiers or amplifying)) and (level near2 (shift or shifting or shifter or shifts or shifted or shifters)) and (parallel or paralleled or parallels) and (input near2 common near2 (mode or modes))	EPO ; JPO ; DER WEN T; IBM _TD _B	2003/01/29 11:02			0